

	Application No.	Applicant(s) SONG ET AL.	
Notice of Allowability	10/702,615		
	Examiner	Art Unit	
	Richard H. Kim	2871	
	Richard H. Nim	2011	
The MAILING DATE of this communication apper All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in or other appropriate commu GHTS. This application is s	this application. If not inclu inication will be mailed in du	ded e course. THIS
1. This communication is responsive to <u>1/3/07</u> .			
2. The allowed claim(s) is/are <u>1,3,4,9,11 and 12</u> .			
 3. Acknowledgment is made of a claim for foreign priority una) All b) Some* c) None of the: Certified copies of the priority documents have Certified copies of the priority documents have Copies of the certified copies of the priority documents have Copies of the certified copies of the priority documents have International Bureau (PCT Rule 17.2(a)). Certified copies not received: 	been received. been received in Applicatio	n No	ation from the
Applicant has THREE MONTHS FROM THE "MAILING DATE" on noted below. Failure to timely comply will result in ABANDONM THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.	of this communication to file ENT of this application.	a reply complying with the r	equirements
4. A SUBSTITUTE OATH OR DECLARATION must be subminiformal PATENT APPLICATION (PTO-152) which give	tted. Note the attached EXA reason(s) why the oath or	MINER'S AMENDMENT or declaration is deficient.	NOTICE OF
5. CORRECTED DRAWINGS (as "replacement sheets") mus	t be submitted.		
(a) I including changes required by the Notice of Draftsperson		(PTO-948) attached	
1) hereto or 2) to Paper No./Mail Date			
(b) including changes required by the attached Examiner's Paper No./Mail Date	Amendment / Comment or	in the Office action of	
Identifying indicia such as the application number (see 37 CFR 1. each sheet. Replacement sheet(s) should be labeled as such in the	84(c)) should be written on the header according to 37 CF	e drawings in the front (not th	ne back) of
 DEPOSIT OF and/or INFORMATION about the deposit attached Examiner's comment regarding REQUIREMENT F 	sit of BIOLOGICAL MATE FOR THE DEPOSIT OF BIO	RIAL must be submitted. LOGICAL MATERIAL.	Note the
Attachment(s) 1. ☐ Notice of References Cited (PTO-892)	5 🗀 Nation of Inf	ormal Patent Application	
2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)	_	mmary (PTO-413),	
	Paper No./I	Mail Date	•
 Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date 12/18/06, 11 /21/05 	7. X Examiner's	Amendment/Comment	
Examiner's Comment Regarding Requirement for Deposit of Biological Material	8. 🔀 Examiner's 9. ☐ Other	Statement of Reasons for All	owance
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Art Unit: 2871

DETAILED ACTION

Election/Restrictions

1. This application is in condition for allowance except for the presence of claims 5-8 directed to a method of manufacturing an array substrate of an LCD device non-elected without traverse. Accordingly, claims 5-8 been cancelled.

EXAMINER'S AMENDMENT

2. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Attorney Eric J. Nuss on 3/28/07.

The application has been amended as follows:

Re claim 1, line 10: between "is" and "connected", insert --directly--.

Re claim 9, line 13: between "is" and "connected", insert --directly--.

Allowable Subject Matter

1. Claims 1, 3, 4, 9, 11 and 12 allowed.

Art Unit: 2871

2. The following is an examiner's statement of reasons for allowance: The prior art of record, taken alone or in combination, fails to teach or disclose in light of the specification, claim 1) an array substrate of an LCD device wherein the array substrate includes a storage capacitor having a lower storage electrode across the data line and in parallel with the gate line on the same layer as the gate line, wherein the lower storage electrode divides the pixel region into two substantially equal sub-regions, and a semiconductor layer interposed between the lower storage electrode and the pixel electrode, wherein the pixel electrode is directly connected to the semiconductor layer by a through hole formed on an upper region of the semiconductor layer; claim 9) an array substrate of an LCD device wherein the lower storage electrode is parallel to the gate line and divides the pixel region into two substantially equal sub-regions, and wherein the pixel electrode is directly connected to the semiconductor layer by the through hole above the semiconductor layer.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Art Unit: 2871

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Richard H. Kim whose telephone number is (571)272-2294. The examiner can normally be reached on 9:00-6:30 M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David Nelms can be reached on (571)272-1787. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Richard H Kim Examiner Art Unit 2871

RHK

David Nelms
Supervisory Patent Examiner
Technology Center 2800